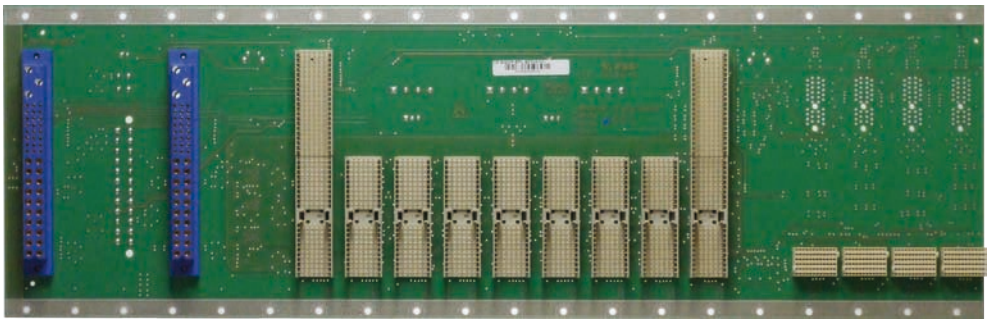
CompactPCI®

EM Compact

EM TRUST
computers you can trust

ICP-BPIOX

PICMG 2.3 R1.0 CompactPCI PlusIO Backplanes



APPLICATIONS

The EMTrust 3U PlusIO backplanes are designed according the PICMG 2.3 R1.0 specification, and provides in addition to parallel PCI bus architecture a simple star architecture based on the specified serial bus standards.

This allows the implementation of hybrid backplanes with both CompactPCI and CompactPCI Serial.

- ▶ High Speed Embedded Systems
- ▶ Avionics/Military
- ▶ Robotics
- ▶ Environment Management
- ▶ Energy Management
- ▶ Industrial Control and Automation
- ▶ Multimedia
- ▶ Traffic and Tranportation
- ▶ Multiprocessing Environments

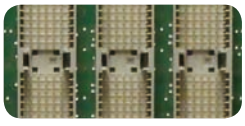
ICP-BPIOX

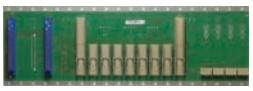
FEATURES

- ▶ Seven CompactPCI 32 bit I/O slots
- ▶ One CompactPCI PlusIO system slot
- ▶ Four CompactPCI Serial peripheral slots with SATA, USB 2.0 and x1 PCIe lane
- ▶ One special system slot connected to the CompactPCI PlusIO slot via ethernet
- ▶ Two reset switches for each of the two system slots
- ▶ Two positronic power modul connectors according to Power Interface Specification PICMG 2.11 R1.0
- ▶ Extremely robust, 3.5 mm thick PCB
- ▶ 3.3V I/O or 5.0 V I/O options

BENEFITS

- ▶ Passive technology for long operational life
- ▶ Industrial rigidity & stability
- ▶ Long-term product availability
- ▶ Superior compatibility
- ▶ Ready for the new high speed serial standard





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OVERVIEW

A passive backplane is a circuit board without any active elements (silicon) but does include peripheral board connection slots into which I/O devices, processors, and other computer and networks components may be installed.

In a passive backplane system, the system bus is used to interconnect a plug-in processor board and multiple plug-in add-on boards.

This architecture makes rapid repair by board substitution possible, and system upgrades and changes are greatly simplified, with minimum resulting system downtime. In fact, the development of passive backplane based systems has been driven by the desire to improve the mean time to repair (MTTR) and to provide an easier path for system upgrade.

SPECIFICATION SUMMARY:

- ► PICMG 2.3 R1.0
- ► 7 32-bit J1 slots
- ► 1 CompactPCI PlusIO Masterslot
- ► 1 standalone CompactPCI Masterslot
- ► 2 Positronic power connectors

SPECIFICATIONS

COMPLIANCE
PICMG 2.3 R1.0

INTERFACES
Seven 32-bit J1 peripheral connectors,
One CompactPCI PlusIO Masterslot,
One standalone CompactPCI Masterslot,
Two 47 pol. Positronic power connectors

PLUG-IN CONNECTORS
2 mm press-fit connectors, grade 2 quality

POWER SUPPLY
ATX 2.2 connector and ATX12V-P4 connector,
CD and HD connectors

SUPPLY VOLTAGE (V I/O)
3.3V / 5 V jumper selected

LAST SLOT TERMINATION
On-board Schottky barrier diodes

TRANSFER MODE
32-bit parallel,
4x PCIe x1

CLOCK FREQUENCY
33 MHz CompactPCI,
PCIe up to 5 Gb/s

PCB THICKNESS
3.5 mm

MASS
Approximately 650 gram

DIMENSIONS
All dimensions are given in mm (height x width)
A 130 x 406

CLIMATIC CONDITIONS
0°C to +70°C (operation)
-40°C to +85°C (storage)
Humidity 0% to 90% @ 40°C (non-condensing)

ORDERING INFORMATION

PRODUCT	DESCRIPTION
ICP-BPIOX	7 32-bit J1 peripheral connectors, 1 CompactPCI PlusIO Masterslot, 1 standalone CompactPCI Masterslot, 2 47 pol. Positronic power connectors

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